

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	PROCESS OPTIONS OF FORMING SILICIDED METAL GATES FOR ADVANCED CMOS DEVICES
Application Number :	
Date :	
First Named Applicant:	Ricky S. Amos
Confirmation Number:	
Attorney Docket Number:	FIS920020157US1

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Documents being submitted:	Files
us-ids	FIS920020157US1-usidst.xml us-ids.dtd us-ids.xsl
us-assignment	FIS920020157US1-usassn.xml us-assignment.xsl us-assignment.dtd FIS92020157US1assign1a.tif FIS92020157US1assign2a.tif FIS92020157us1assignm3of4.tif FIS92020157US1assignpg4.tif
us-request	FIS920020157US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	FIS920020157US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	FIS92020157US1dec1.tif
us-declaration	FIS92020157US1dec3.tif
application-body	Fis9020157US1b-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd FIS9020157us1draw1.tif FIS9020157us1draw2.tif FIS9020157us1draw3.tif

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FIS9020157us1draw16.tif
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us-declaration

Comments

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